### REMARKS

Claims 1-3 and 6-11 remain pending in connection with the present application. Claims 1 and 9 are independent claims.

This Supplemental Amendment hereby incorporates the entire contents of the Amendment filed July 24, 2006 by reference. The Supplemental Amendment is merely being filed to correct minor informalities to claim 1, to clarify the claim by now stating that "voltages" (not voltage values) are applied (noting that the storage means is for storing "values" of voltages).

With regard to the traversal of the various objections and rejections, the same arguments set forth in the Amendment filed July 24, 2006 apply. With regard to the traversals of the prior art rejections, the arguments have been modified slightly as set forth below, consistent with the above-mentioned changes to claim 1, and therefore replace the arguments set forth in the Amendment filed July 24, 2006.

#### **Description of an Example Embodiment**

An example embodiment of the present invention is directed to an LCD. In one example embodiment, to lessen or even avoid image flicker due to variations of the driving voltage for example, the voltage applied to the common electrode or source-electrode, is shifted to match the center of a voltage waveform of the common electrode with the center of the voltage waveform of the source electrode. To accomplish this task in various modes of an LCD screen, the voltage applied to the electrodes is adjusted to match various waveforms. In FIG. 1 for example, memory 16 stores the various mode values used to adjust the voltage waveforms for the various LCD modes of operation. By shifting the waveforms of the voltages applied to the row and column electrode drive circuits, it is possible to obtain multiple variations.

## **PRIOR ART REJECTIONS**

# 35 U.S.C. §102(b) Yer Rejection

Claims 1 and 3 stand rejected under 35 U.S.C. §102(b) as being anticipated by Yer et al. (U.S. PG Pub. No 2002/0109655). Applicants respectfully traverse this rejection.

Yer is directed to a driving circuit for an LCD device that responds to environmental changes by adjusting the voltage levels provided to the source driver in response to changes indicated by the environmental sensor 83. Changes are identified by environment sensor 83 and passed to controller 85, which instructs the programmable gamma voltage generated to adjust the voltage output provided to the source driver.

In Yer, the programmable gamma voltage generator includes a memory 81a. Data concerning gamma amendment are stored in the memories (memory 81a and digital variable resistor 81b) in order to obtain an exact display corresponding to a change of the circumferential environment. Based on the output of memory 81a, the resistance value of digital variable resistor 81b is adjusted, thereby changing the output voltage from the gamma voltage outputting unit 81c.

Applicants submit that Yer fails to teach or suggest at least "...applying the read optimum voltage to shift the voltage waveform of the electrode" (emphasis added), as recited in independent claim 1, the with the voltage values being stored. In Yer, only the amplitudes of the output voltage are adjusted in response to changes from the environmental sensor. Thus, Yer merely stores data in the memories to obtain an exact display corresponding to change of the circumferential environment, and does not store voltage values and apply of voltages to shift the voltage waveform of the electrode.

Accordingly, Applicants respectfully submit that Yer fails to disclose, teach, suggest or render obvious each and every feature of independent claim 1; and respectfully requests that the rejection of independent claim 1 and dependent claim 3, under 35 U.S.C. §102(b), be withdrawn.

### 35 U.S.C. §103(a) Yer Rejection

Claims 2 and 6-8 stand rejected under 35 U.S.C. §103(a) as being anticipated by Yer, which Applicants assume is due to obviousness. Applicants respectfully traverse this rejection.

As previously described, Yer does not disclose, teach or suggest at least the feature of "...applying the read optimum voltage to shift the voltage waveform of the electrode" as recited in independent claim 1. Dependent claims 2 and 6-8 depend from independent claim 1, and therefore, include the features of independent claim 1.

Applicants submit that Yer does not render obvious the features of independent claim 1. Therefore, Applicants respectfully request the rejection of independent claims 2 and 6-8, under 35 U.S.C. §103(a), be withdrawn.

Applicants further submit that relying on common knowledge or common sense of a person of ordinary skill in the art, without any specific hint or suggestion of this in a particular reference, is not a proper standard for reaching the conclusion of obviousness. 1 If the Examiner is relying on personal knowledge to support a finding of what is known in the art, the Examiner must provide an affidavit or declaration setting forth specific factual statements and explanations to support these findings.<sup>2</sup>

In view of the above arguments, Applicants assert that the Examiner has failed to show a prima facie case of obviousness under 35 U.S.C. §103(a).

<sup>&</sup>lt;sup>1</sup> See, <u>In re Sang Lee</u>, 61 USPQ 2d, 1430 (Fed. Cir. 2002).
<sup>2</sup> See, 37 C.F.R. §1.04(d)(2) and MPEP §2144.03(c).

Application No. 10/660,567 Attorney Docket No. 12480-000021/US

**NEW CLAIMS** 

For reasons somewhat similar to those set forth above, Applicants submit that Yer does

not teach, suggest or render obvious, the subject matter of new claims 9-11 (which are dependent

on independent claim 1 and thus allowable for reasons set forth therein). Accordingly,

Applicants submit that new claims 9-11 are patentable.

**CONCLUSION** 

Accordingly, in view of the above amendments and remarks, reconsideration of the

objections and rejections and allowance of each of claims 1-3 and 6-11 in connection with the

present application is earnestly solicited.

Should there be any outstanding matters that need to be resolved in the present

application, the Examiner is respectfully requested to contact the undersigned at the number

listed below.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future

replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any

additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension

of time fees.

Respectfully submitted,

HARNESS, DICKEY, & PIERCE, P.L.C.

By

Donald J. Daley, Reg. No. 34,313

P.O. Box 8910

Reston, Virginia 20195

(703) 668-8000

DJD:lak